



# BTW09

## IEEE 8<sup>th</sup> Intl. Board Test Workshop

Intel, Fort Collins, Colorado USA

September 15 - 17, 2009



### Call for Papers

#### Organizing Committee

##### General Chair

Bill Eklow - Cisco Systems, Inc.

##### General Vice Chair

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##### Local Arrangements

Kevin Daverin - Intel

Jeff Huff - Intel

Following the very successful 7<sup>th</sup> Board Test Workshop (BTW) held in Fort Collins, the BTW Organizing Committee is organizing the seventh BTW event, which will be hosted in Fort Collins, Colorado. BTW09 will focus on current issues and trends related to board test. Similar to prior Board Test workshops, perspectives are invited from contract manufacturers, test equipment providers, researchers, end users and systems providers.

BTW09 is sponsored by the IEEE Philadelphia Section, in cooperation with the IEEE Computer Society and the Test Technology Technical Council (TTTC) and is supported by the Board Test Technical Activities Committee (BTTAC)

We cordially invite you to participate and submit your contribution to BTW09, which includes (but is not limited to) the following topics:

#### Traditional Board Test Techniques

Ⓞ *Boundary Scan-based test techniques*

Ⓞ *Assessing BSDL compliance and accuracy*

Ⓞ *Board test: structural versus functional test*

Ⓞ *Board Flex issues*

Ⓞ *Parametric testing*

Ⓞ *OnBoard Built-In Self Test techniques*

Ⓞ *Electrical, Optical and X-Ray board test mix*

Ⓞ *Board test for high-volume consumer products*

Ⓞ *Advances in flying probe technology*

Ⓞ *Creating board functional tests*

Ⓞ *Advances in ICT fixturing/bead probes*

Ⓞ *Interfacing test flows with manufacturing flows*

Ⓞ *Linking ICT with low-cost PC testers*

Ⓞ *Fault-coverage metrics*

Ⓞ *Reducing false fails*

Ⓞ *Applications of VXI/PXI plug 'n play technology*

#### In-System Configuration

Ⓞ *OnBoard programmability of CPLDs/FPGAs*

Ⓞ *How to program PLDs to execute test functions*

#### New Standards and New Problems

Ⓞ *Practical use of P1581 SCITT*

Ⓞ *Advances in 1149.6 AC-EXTEST*

Ⓞ *Advances in 1149.4 Mixed-Signal test Bus*

Ⓞ *P1687 (IJTAG)*

Ⓞ *SJTAG*

Ⓞ *Test techniques for SERDES boards*

#### Outsourcing to EMS companies

Ⓞ *Outsourcing to EMSs – issues and solutions*

Ⓞ *Re-using prototype tests in volume manufacturing*

Ⓞ *At-speed board and system structural test*

#### System-Level and Field-Service Test

Ⓞ *Using 1149.1 as a backplane test-bus*

Ⓞ *1149.1 backplane test-bus support devices*

Ⓞ *Systems design integration and test issues*

Ⓞ *Field servicing: test needs and solutions*

#### Board and System Test Economics

Ⓞ *Board test economics*

Ⓞ *Emulation in a field-service environment*

Ⓞ Board test: educational requirements

**Submissions:** As with last year's BTW, there will be three opportunities to participate (paper, presentation or short "Elevator talk"). **All attendees are encouraged to participate.** For papers and presentations, the Program Committee invites original submissions of at least 1,000 and at most 5,000 words focusing on new concepts and methodologies. Full paper submissions are preferred and recommended but the Program Committee will accept extended abstracts. A one paragraph submission is required for Elevator Talks.

Electronic submission in Adobe PDF via the workshop web page (see below) is mandatory. The web site also informs about the *review criteria*. Please identify a contact author with complete information (mailing address, phone number, fax number and e-mail address).

**Proceedings:** Following the event, BTW09 will post presentations and papers from the accepted contributions whose authors wish to provide the corresponding materials on the BTW web site.

#### Deadlines and Key Dates:

- Submission of full papers or extended abstracts: **July 25<sup>th</sup>, 2009**
- Notification of acceptance: **August 15<sup>th</sup>, 2009**
- Final paper due: **September 5<sup>th</sup>, 2009**

#### Further Information

The **BTW09** web site will be available soon, go to: <http://www.molesystems.com/BTW09>

The web site will provide more information on the workshop and how to submit papers.

To find out more about the IEEE Computer Society Board Test Technical Activities Committee (BTTAC) go to: [http://www.tttcdb.org/ttc\\_website/index.php?section=activities\\_board\\_test](http://www.tttcdb.org/ttc_website/index.php?section=activities_board_test)